

1/25

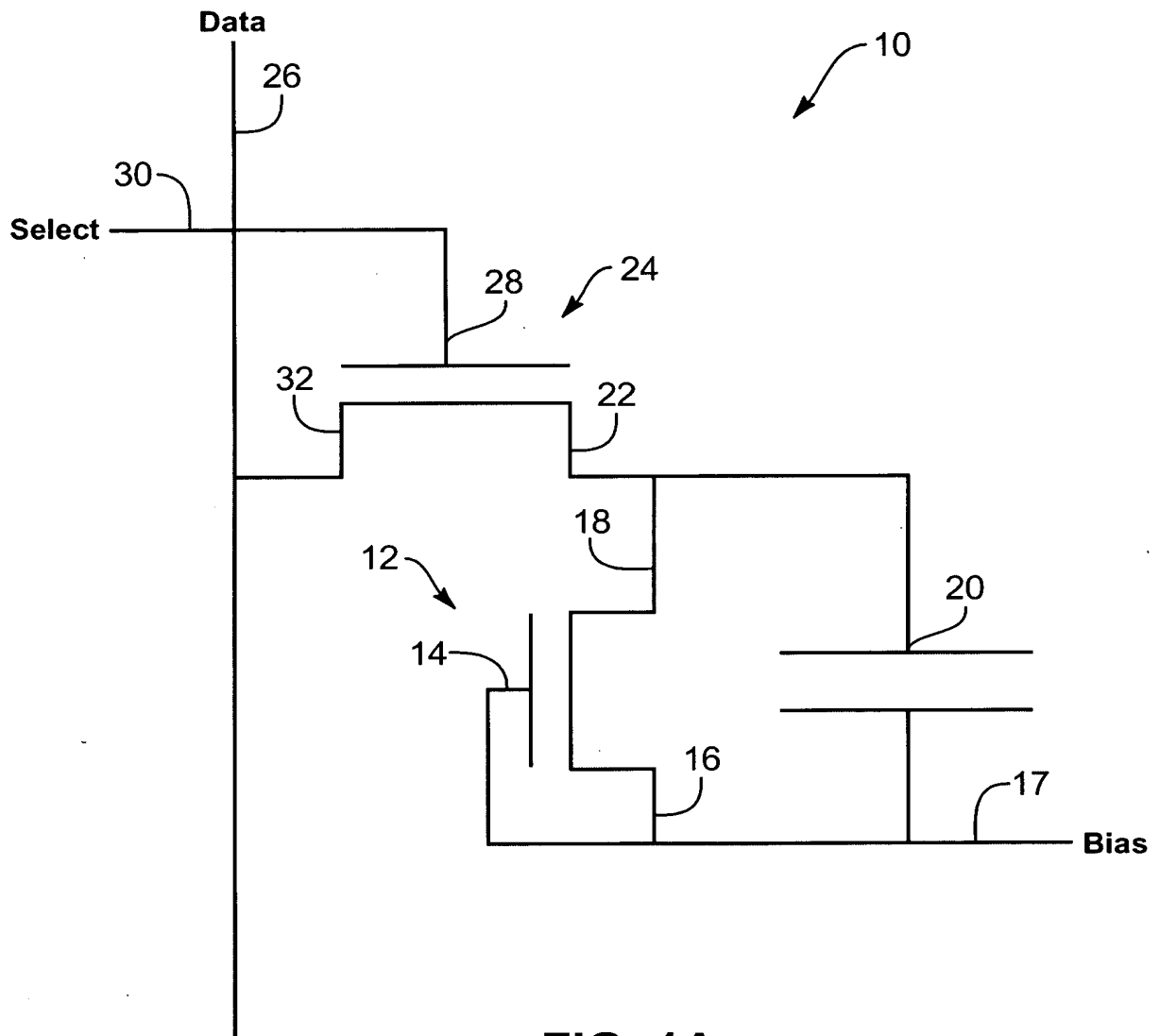


FIG. 1A

2/25

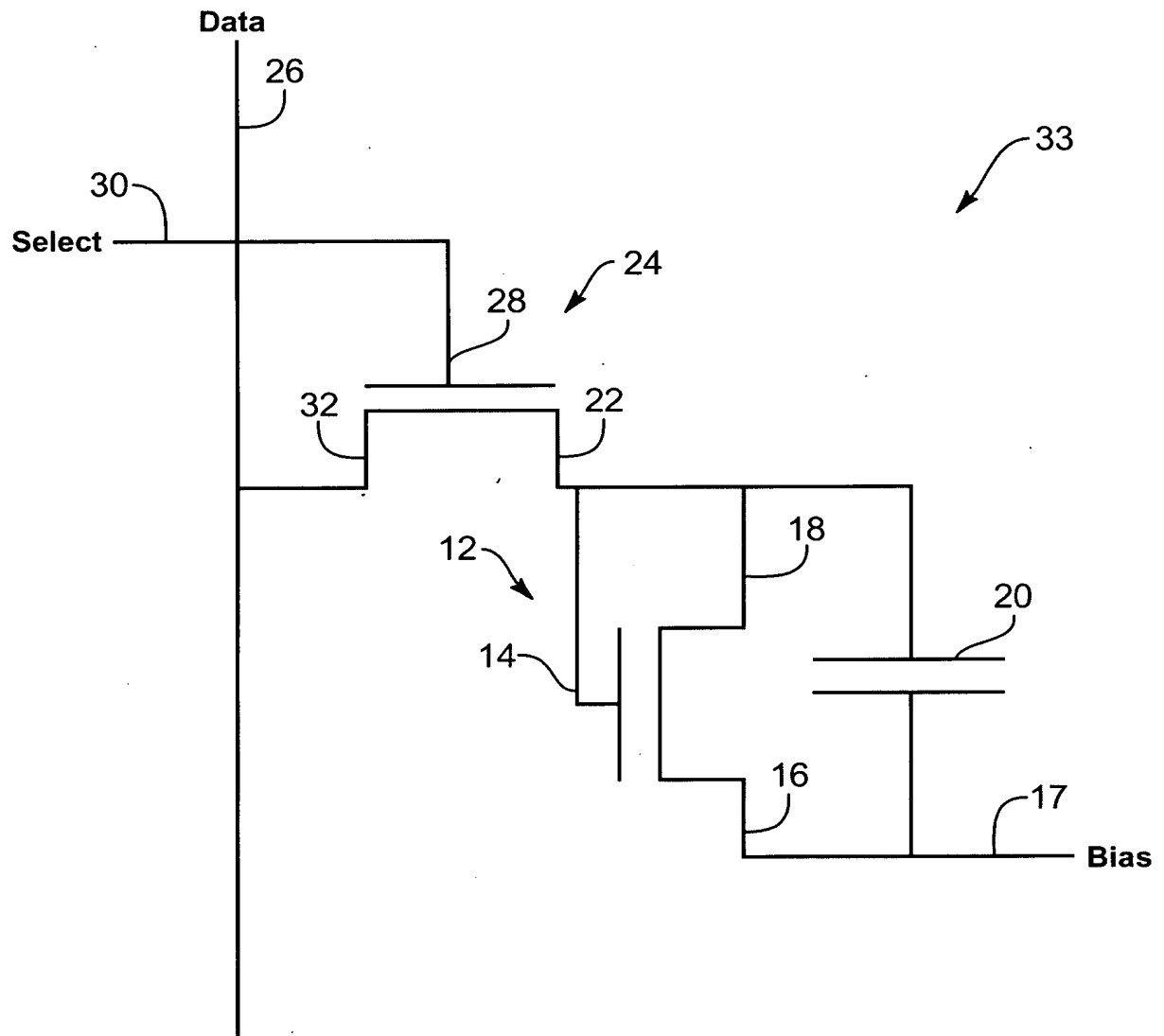


FIG. 1B

3/25

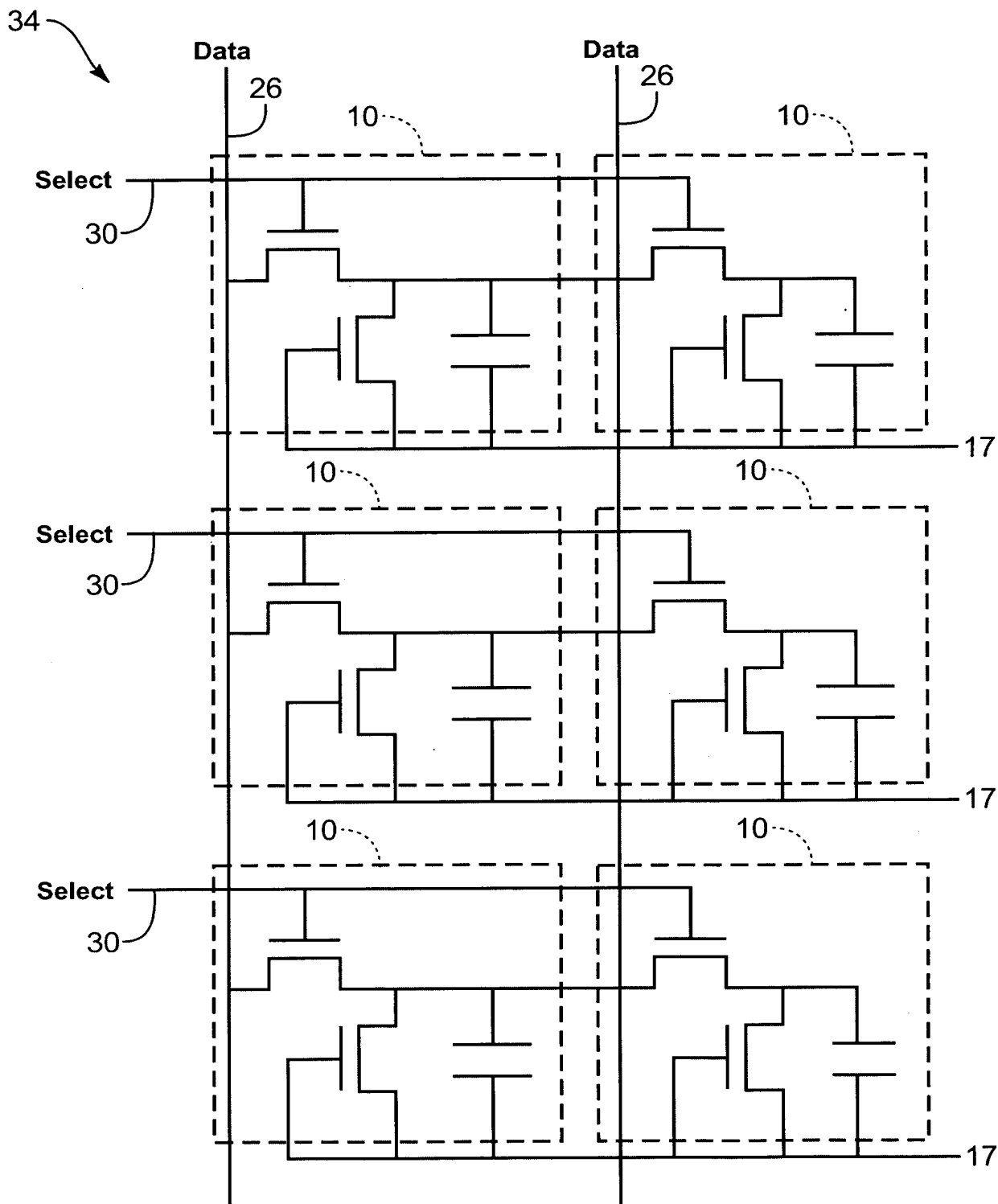


FIG. 2

4/25

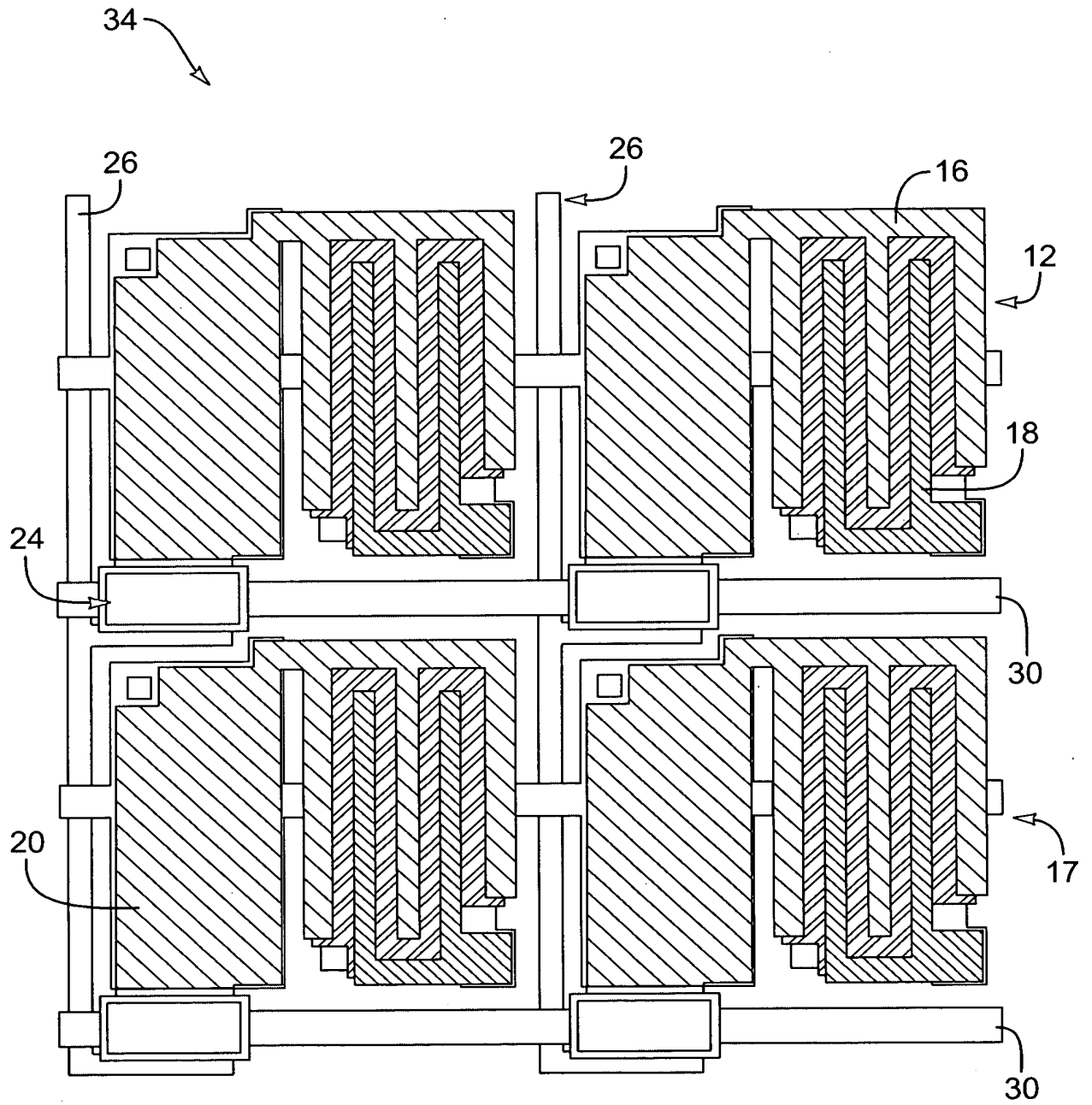


FIG. 3

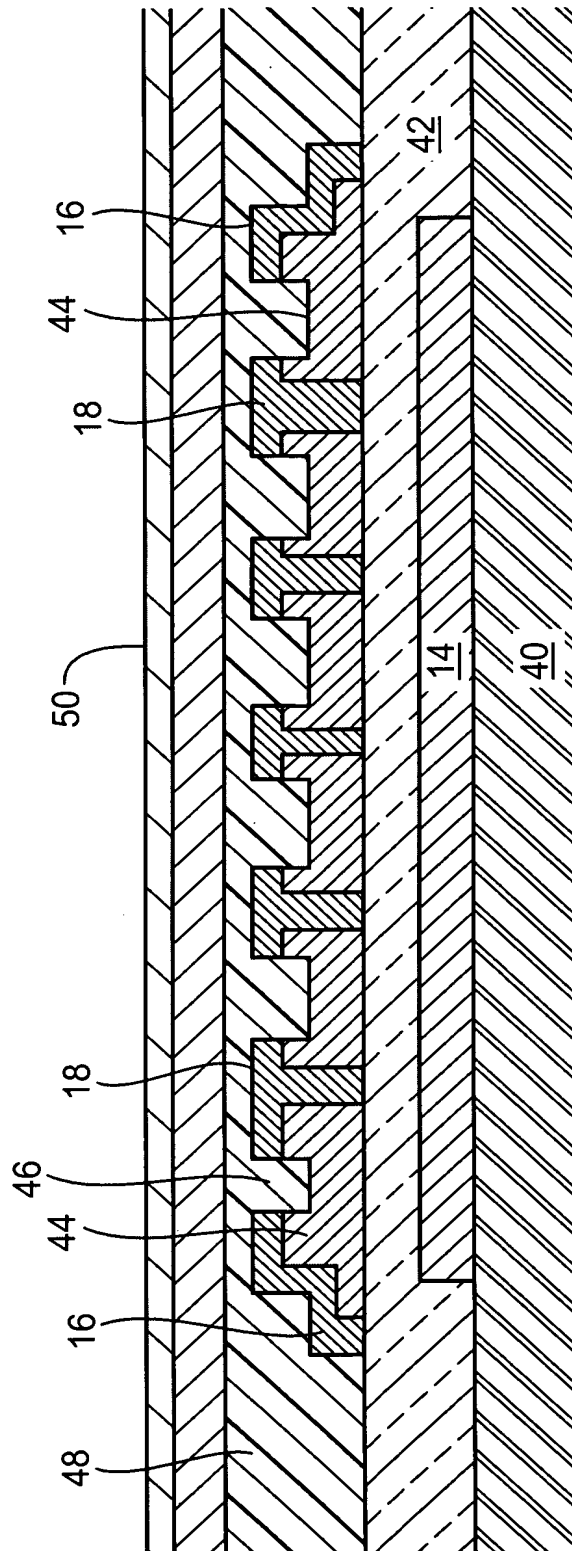


FIG. 4

6/25

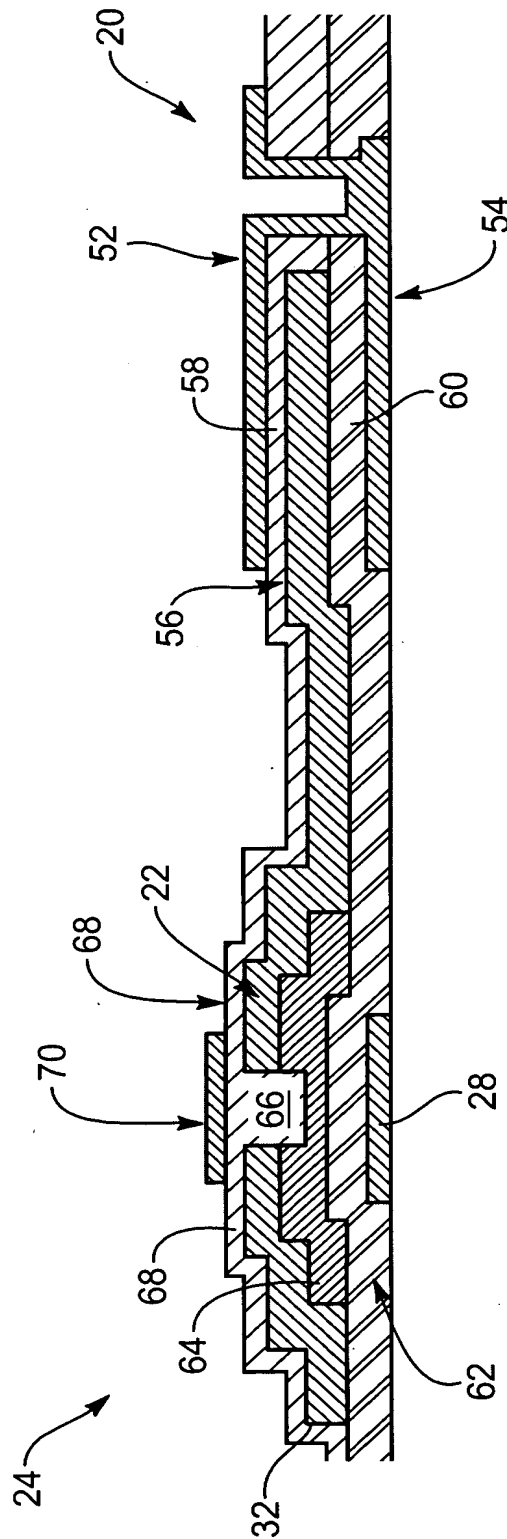


FIG. 5

7/25

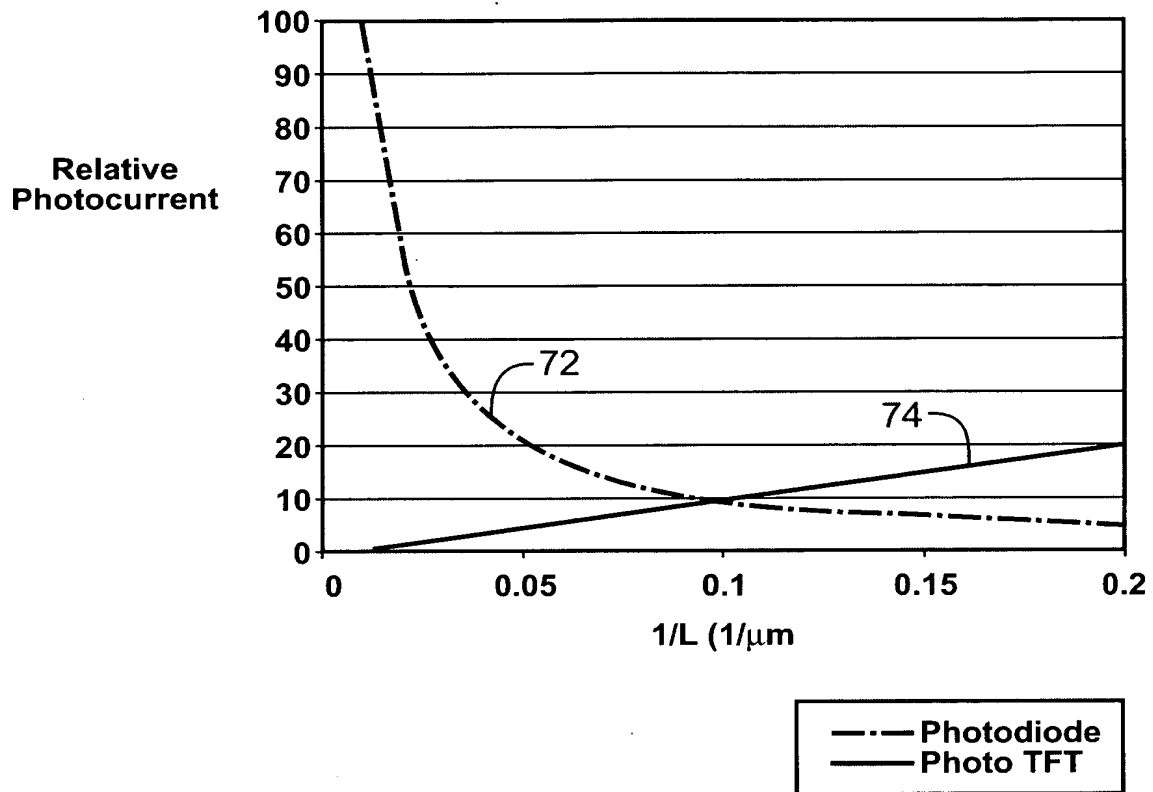


FIG. 6

8/25

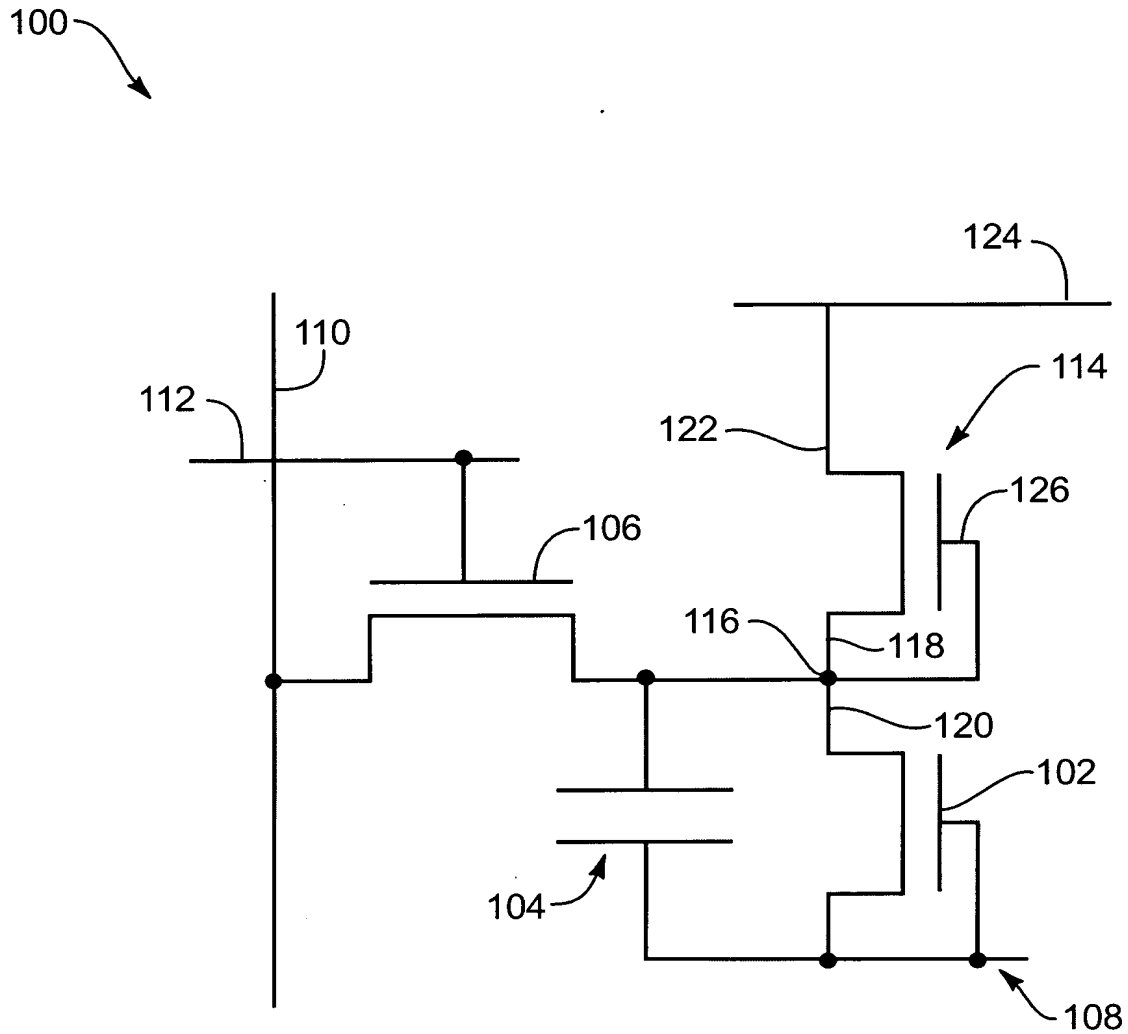


FIG. 7

9/25

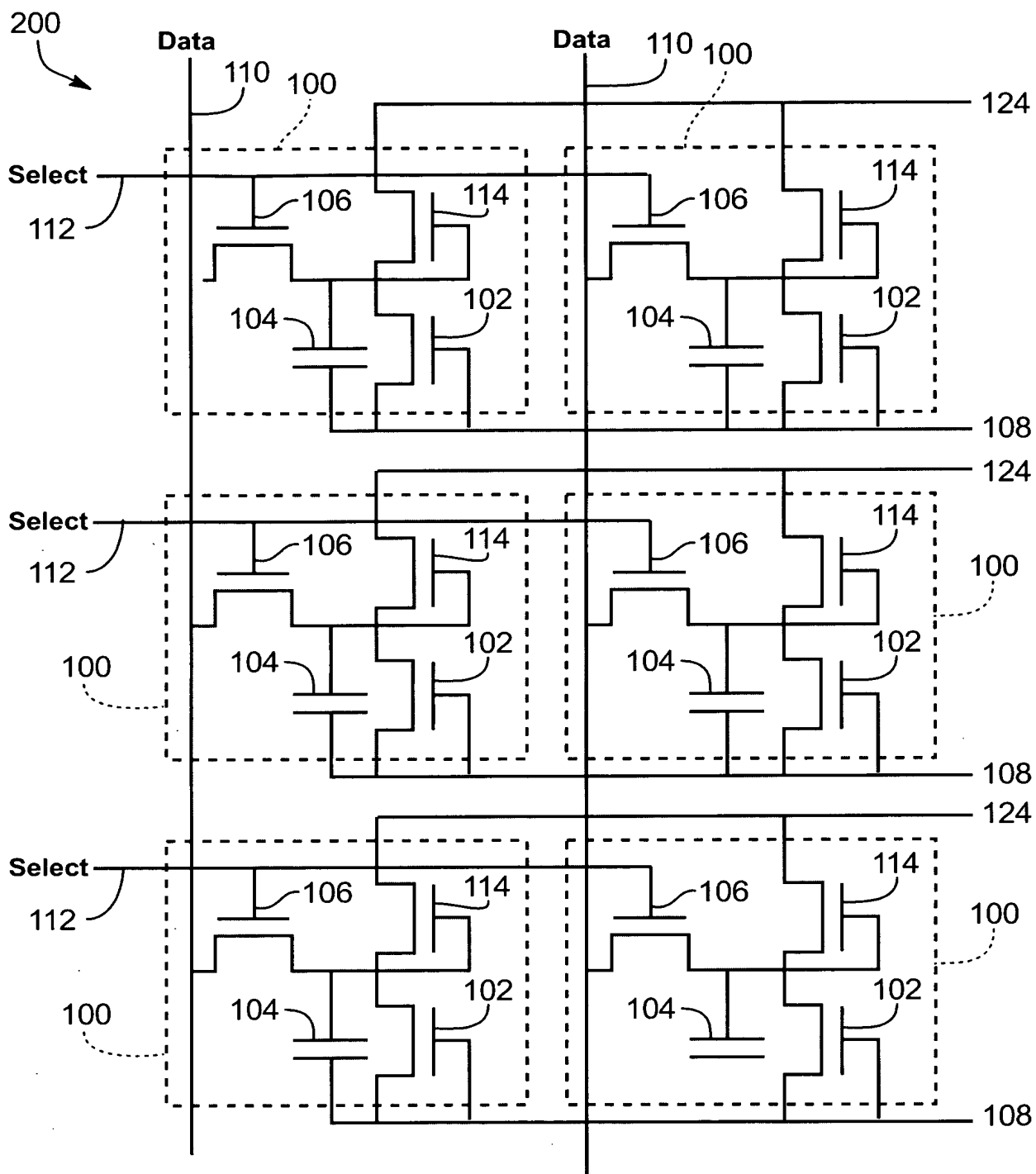


FIG. 8

10/25

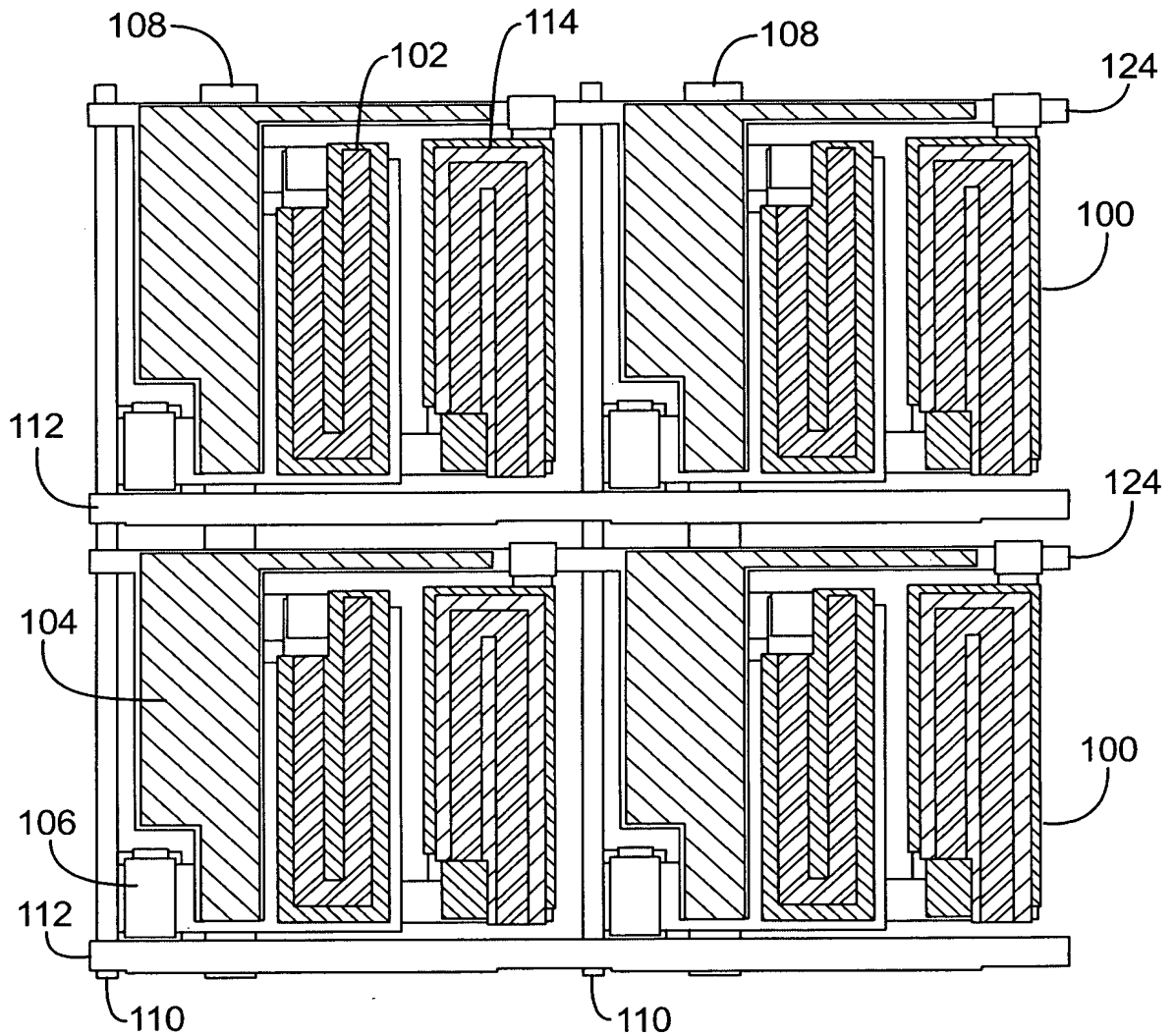


FIG. 9

11/25

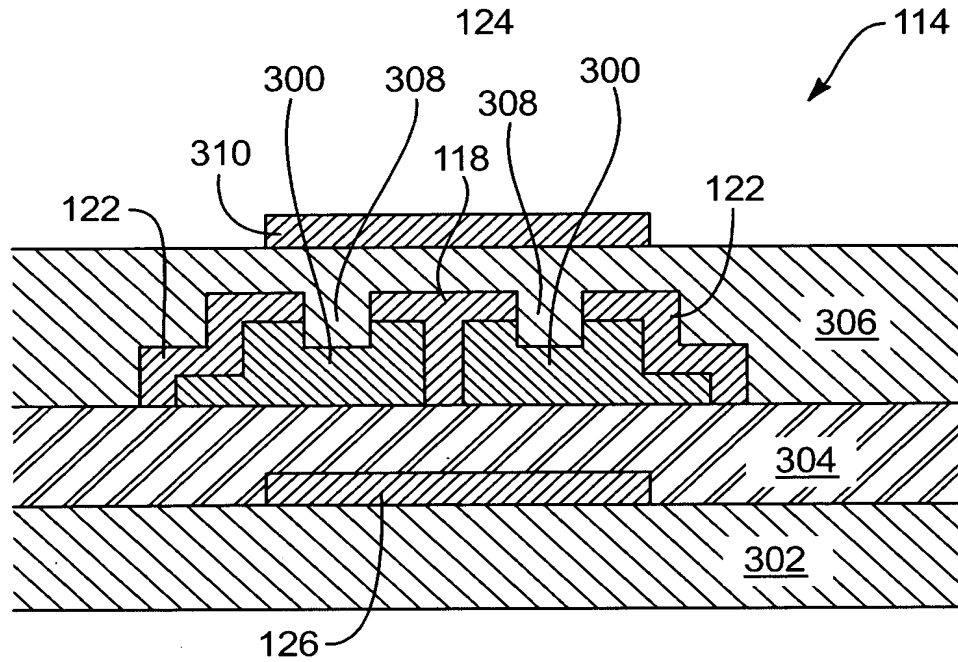


FIG. 10

12/25

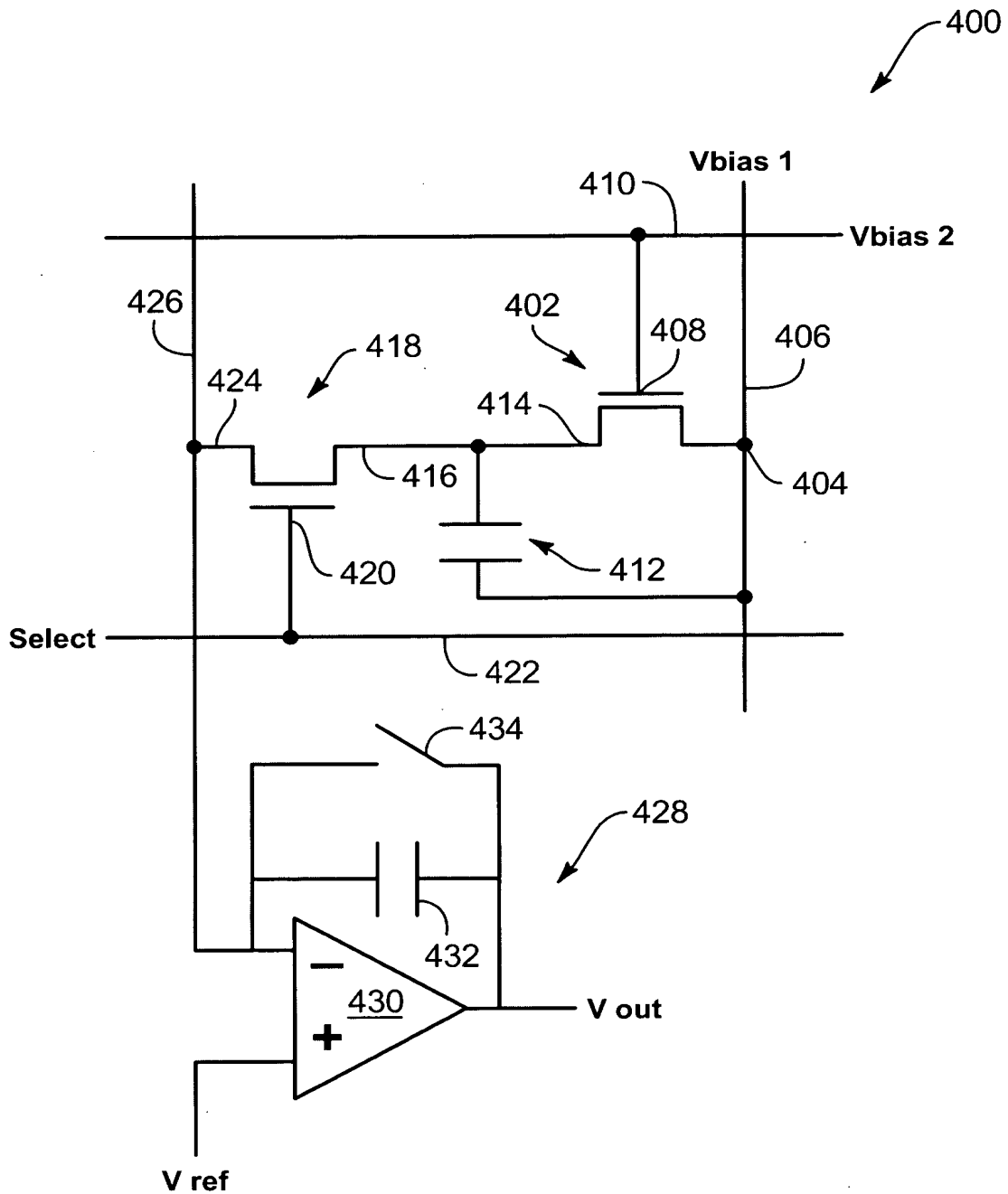


FIG. 11

13/25

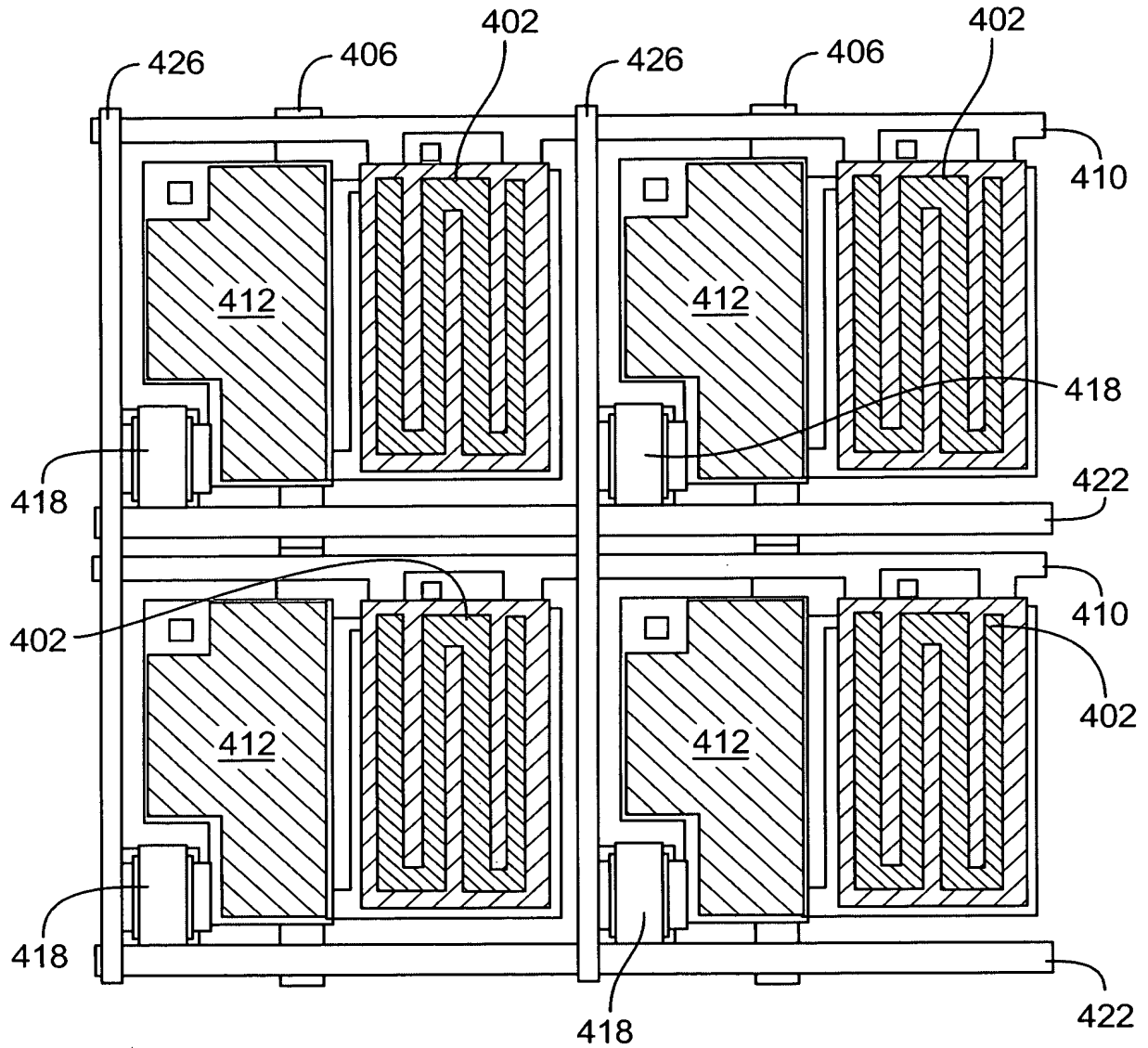
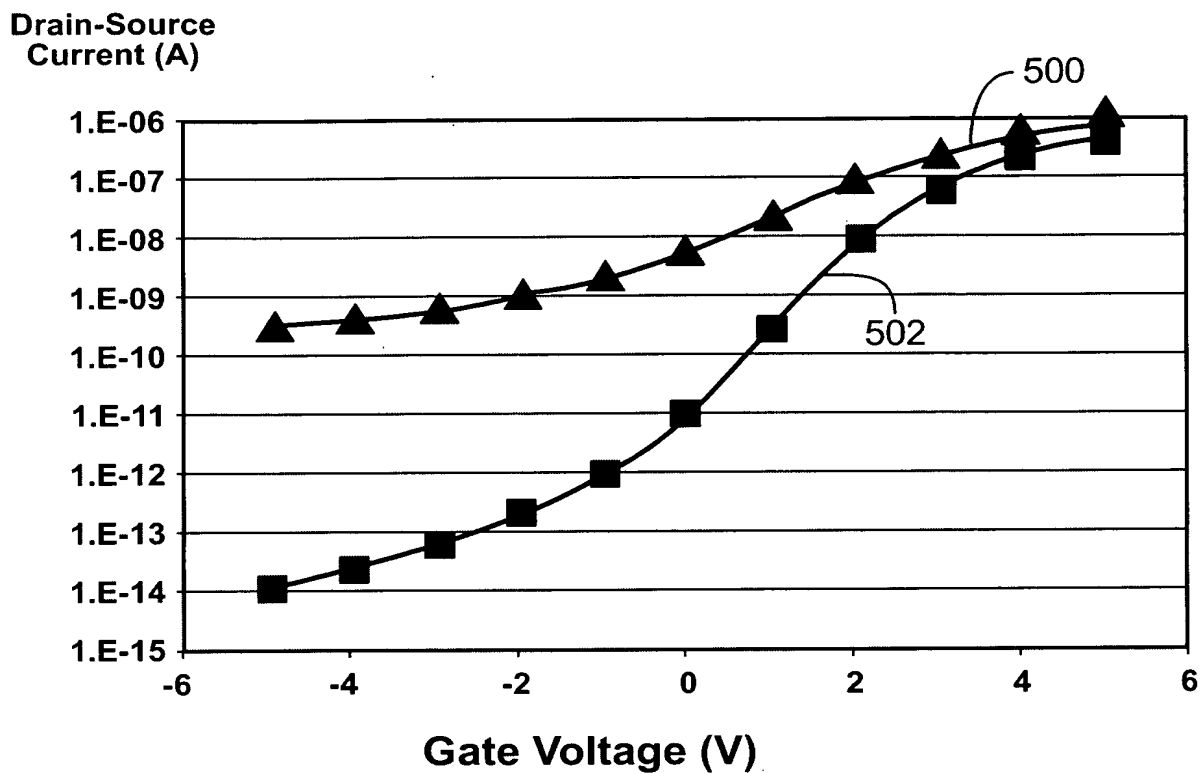


FIG. 12

14/25



Dark Current and Photocurrent vs Gate Voltage (V_{bias}) at $V_{ds}(=V_{bias1})+5V$ for a-Si TFT with $W/L = 200 \mu m/6 \mu m$

FIG. 13

15/25

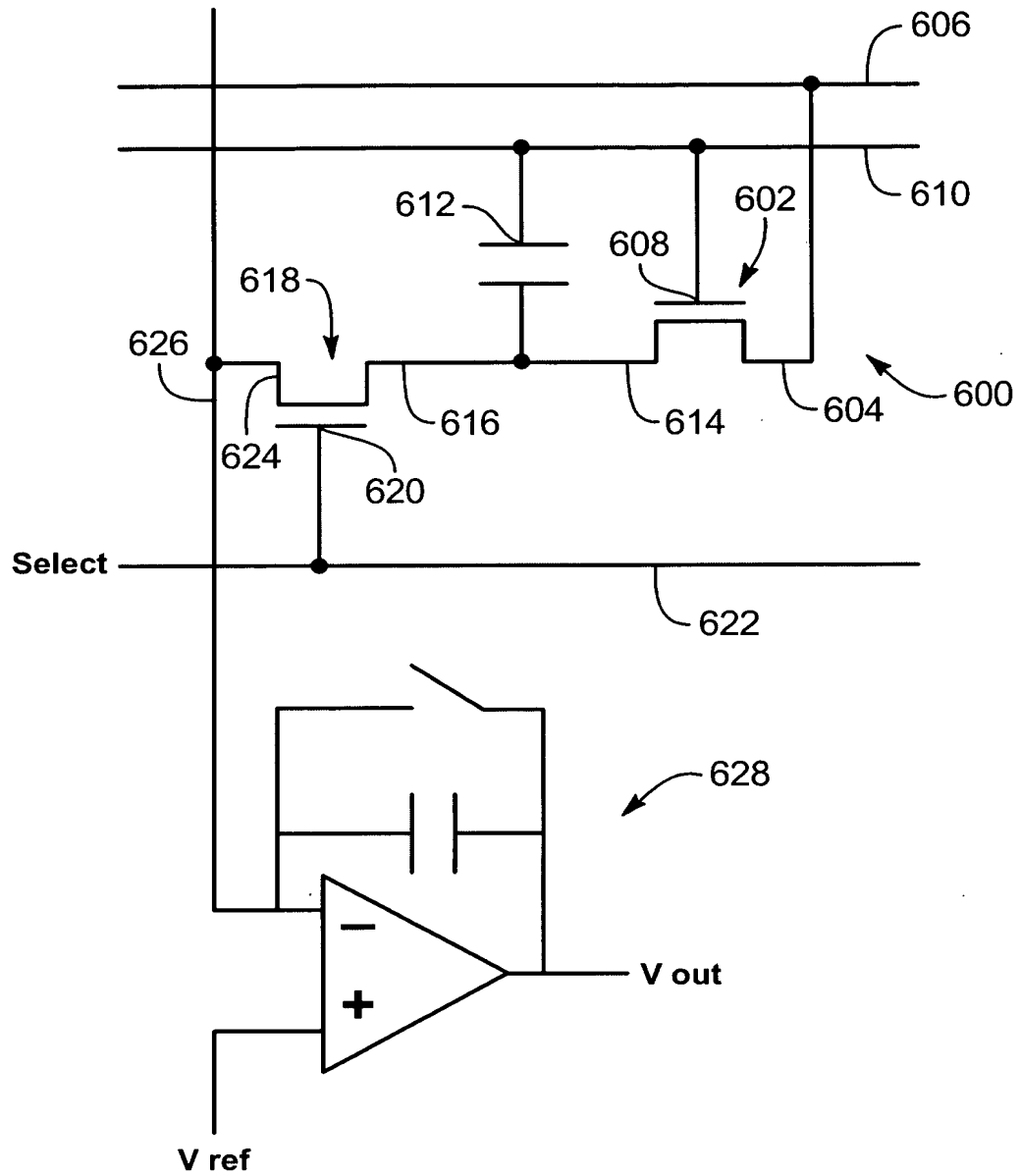


FIG. 14

16/25

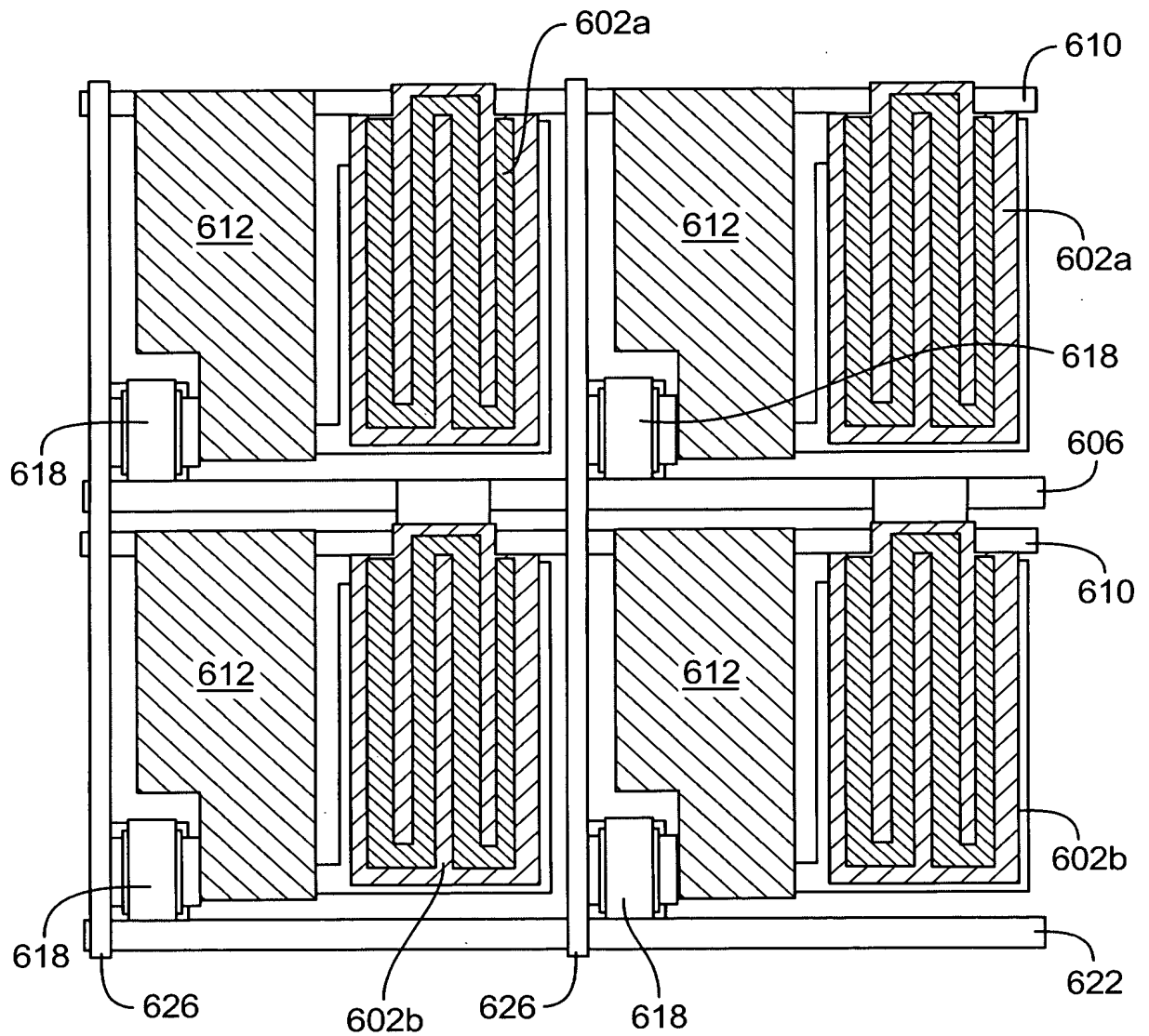


FIG. 15

17/25

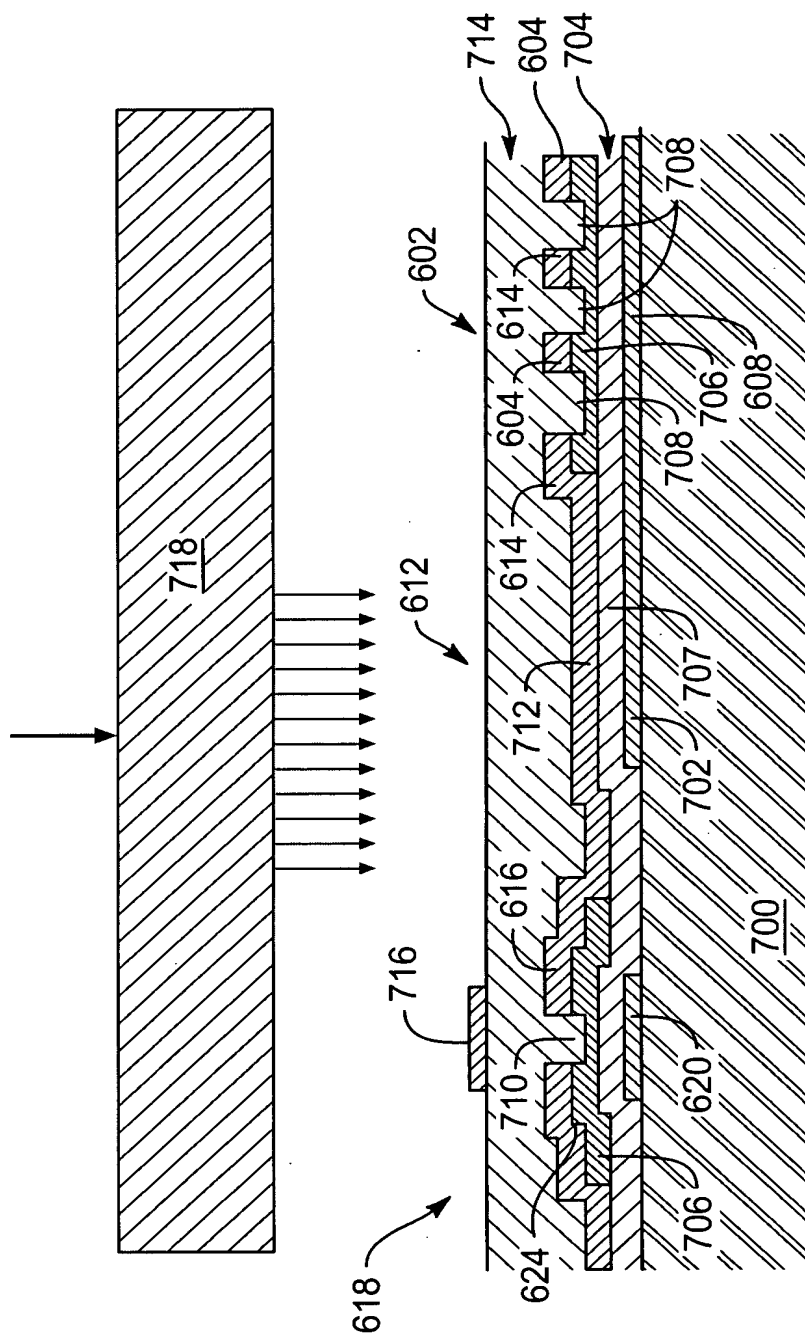


FIG. 16

18/25

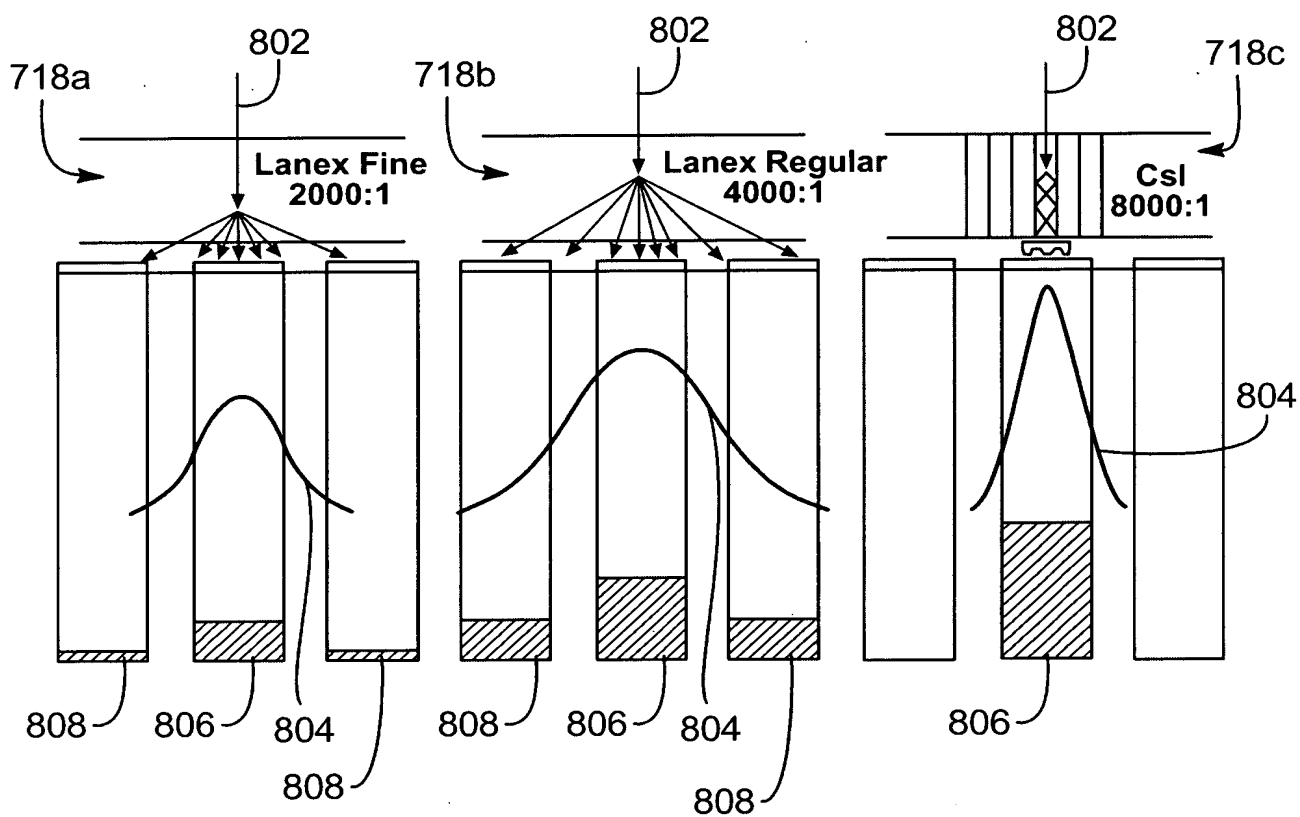


FIG. 17

19/25

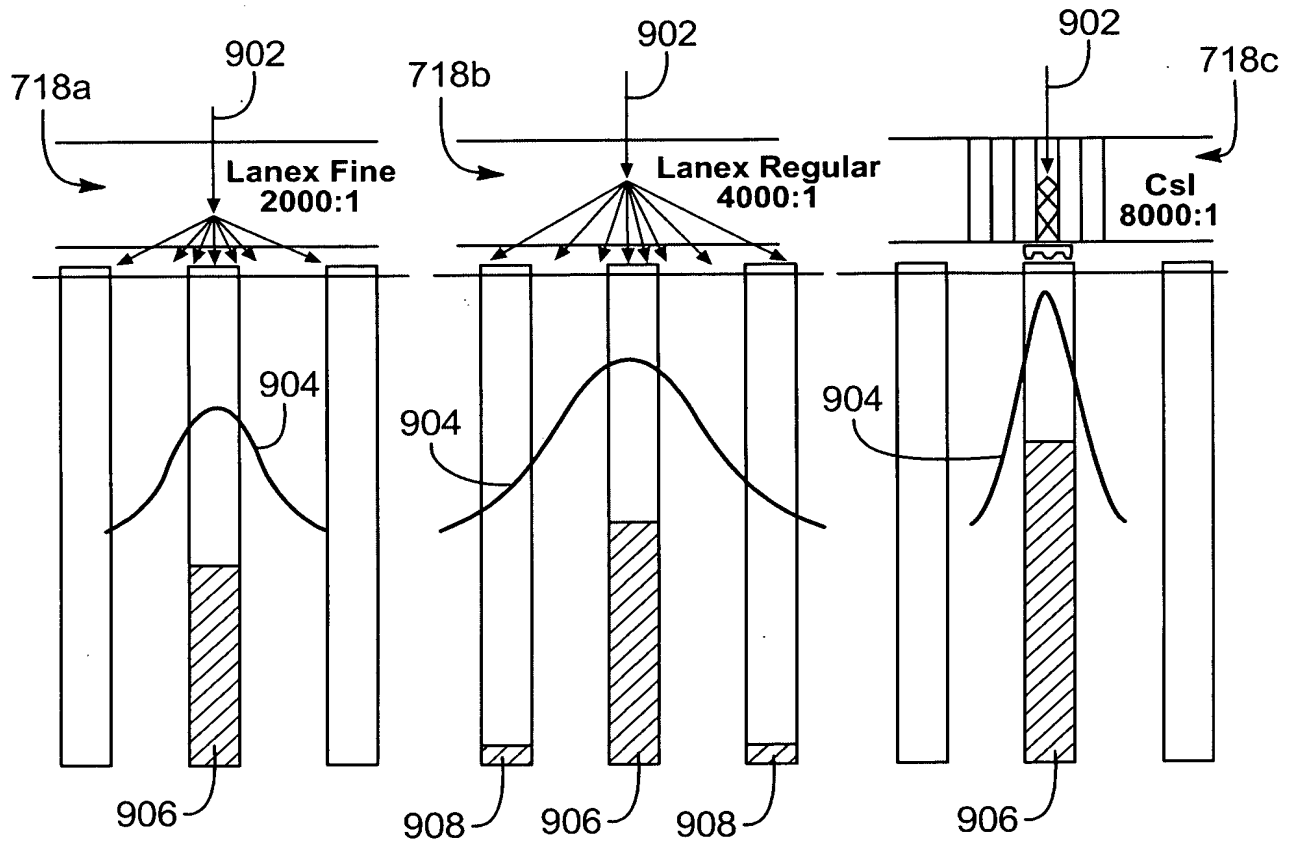


FIG. 18

20/25

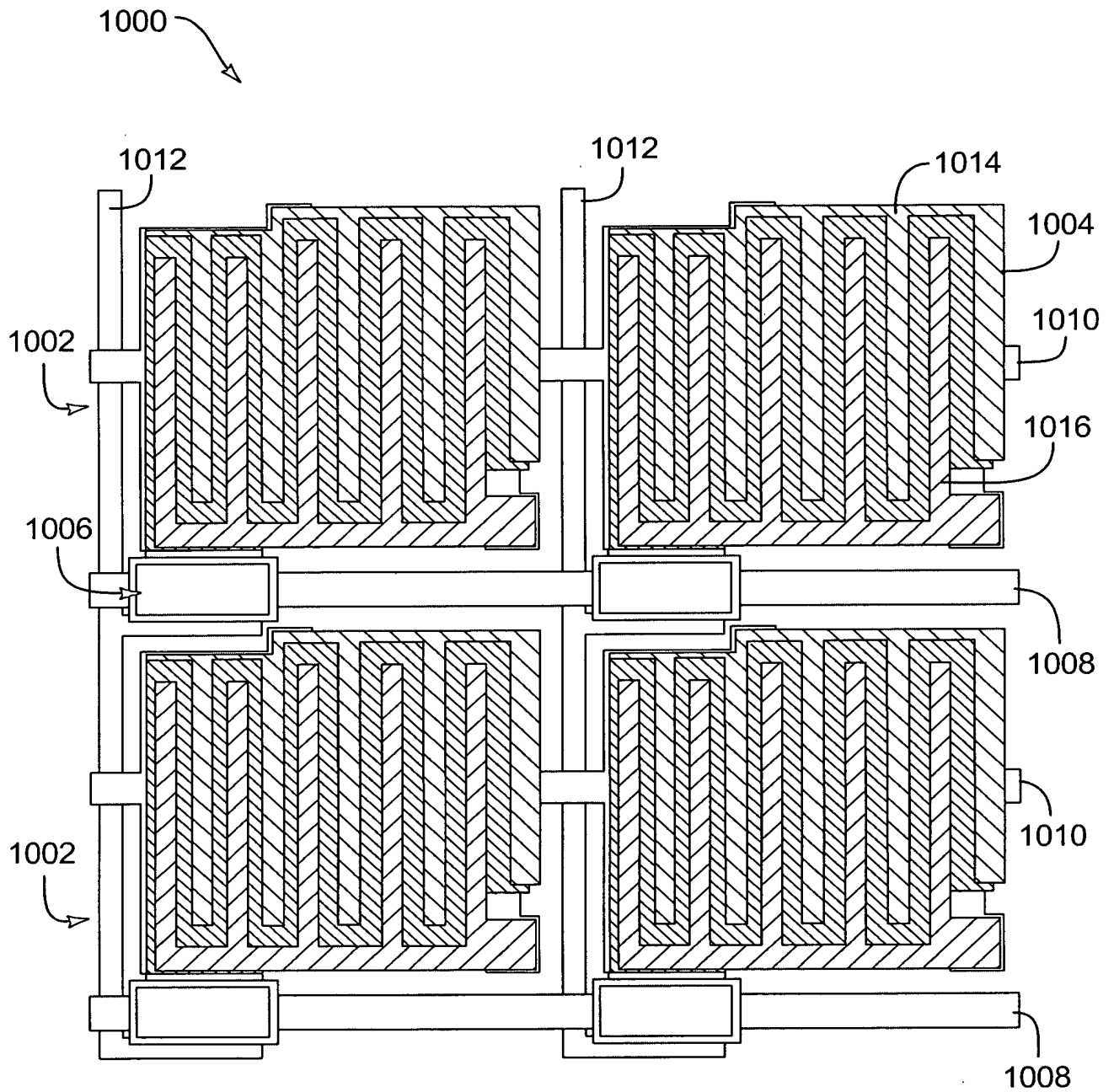


FIG. 19

21/25

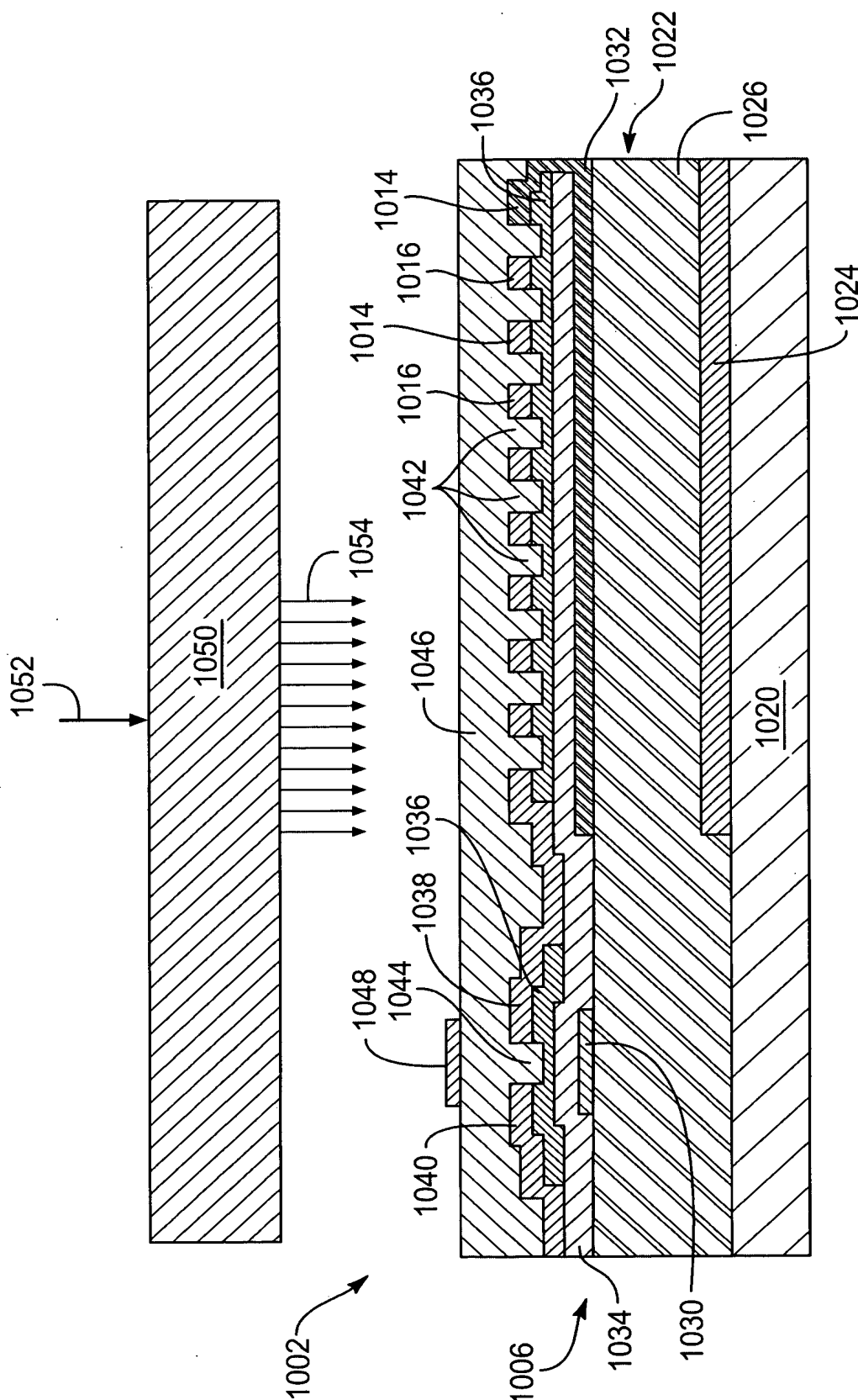


FIG. 20

22/25

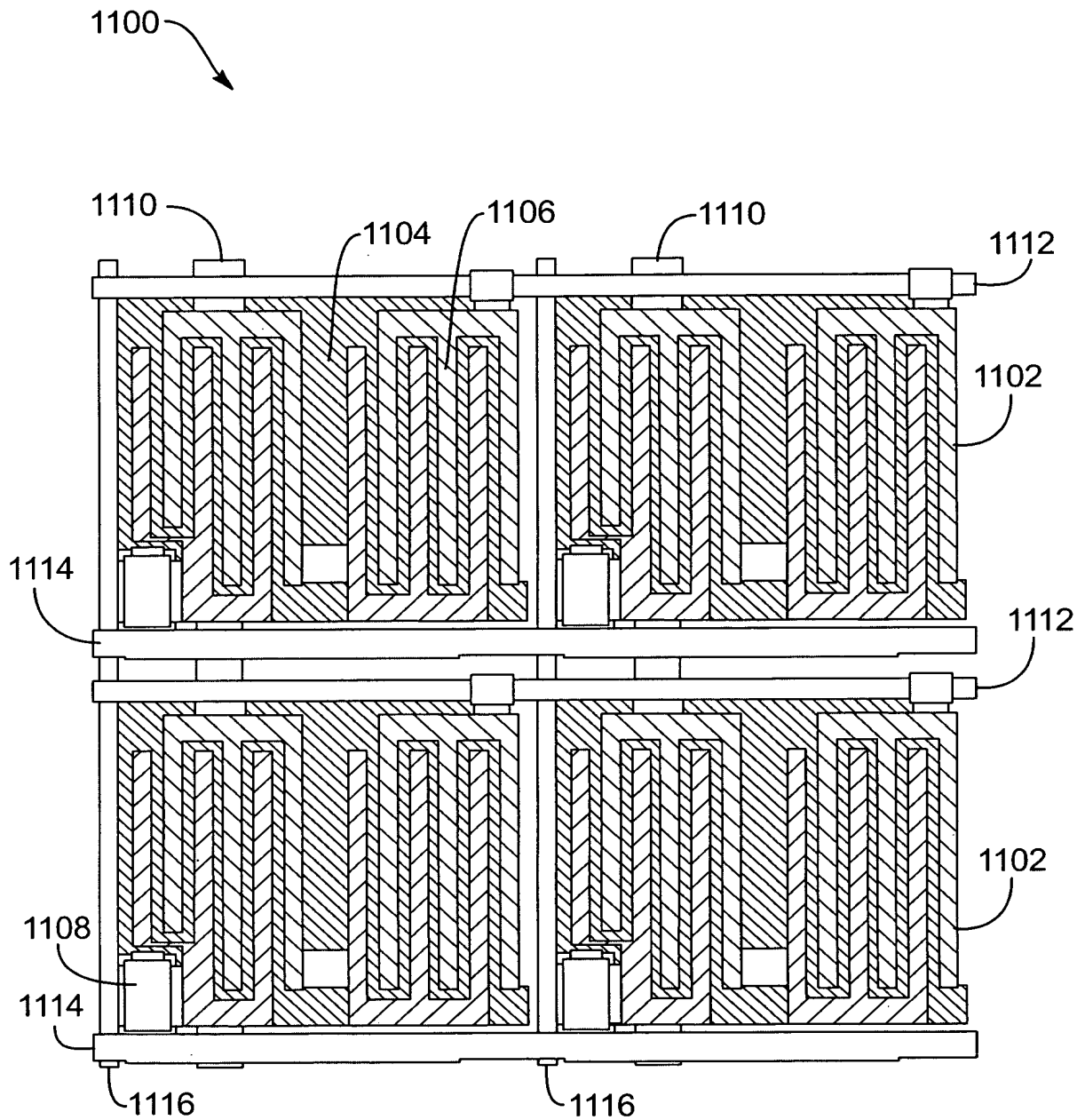


FIG. 21

23/25

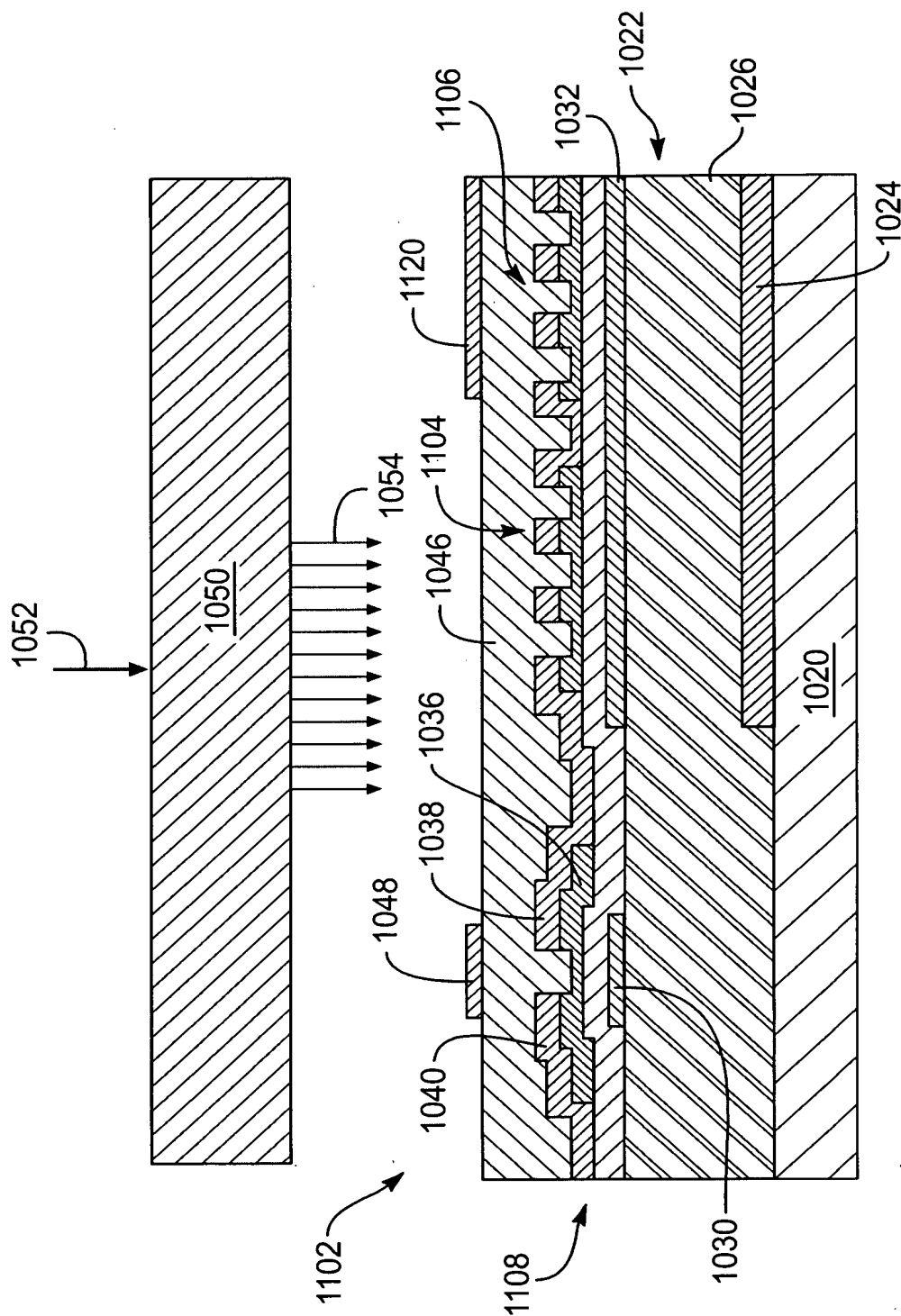


FIG. 22

24/25

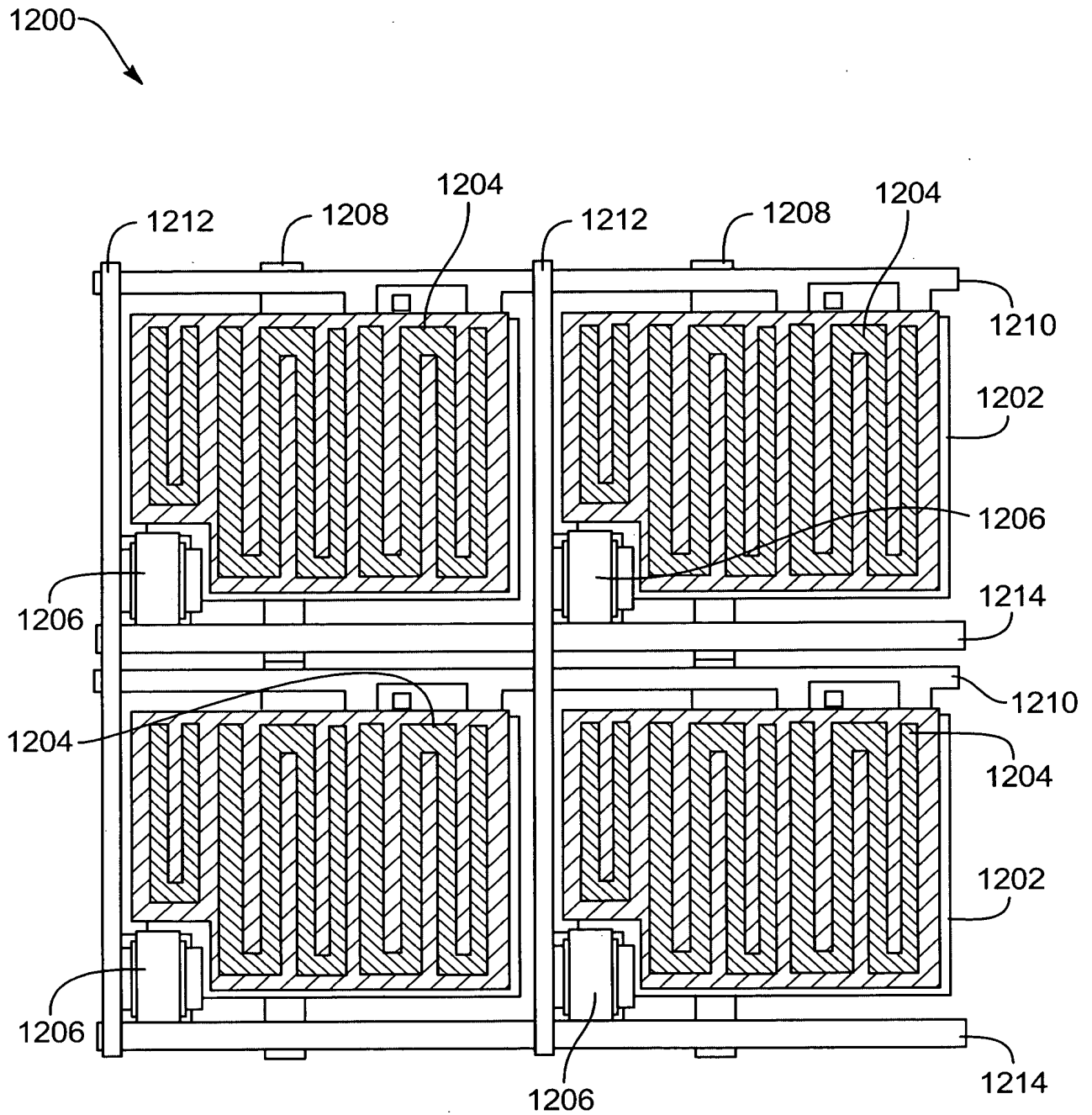


FIG. 23

25/25

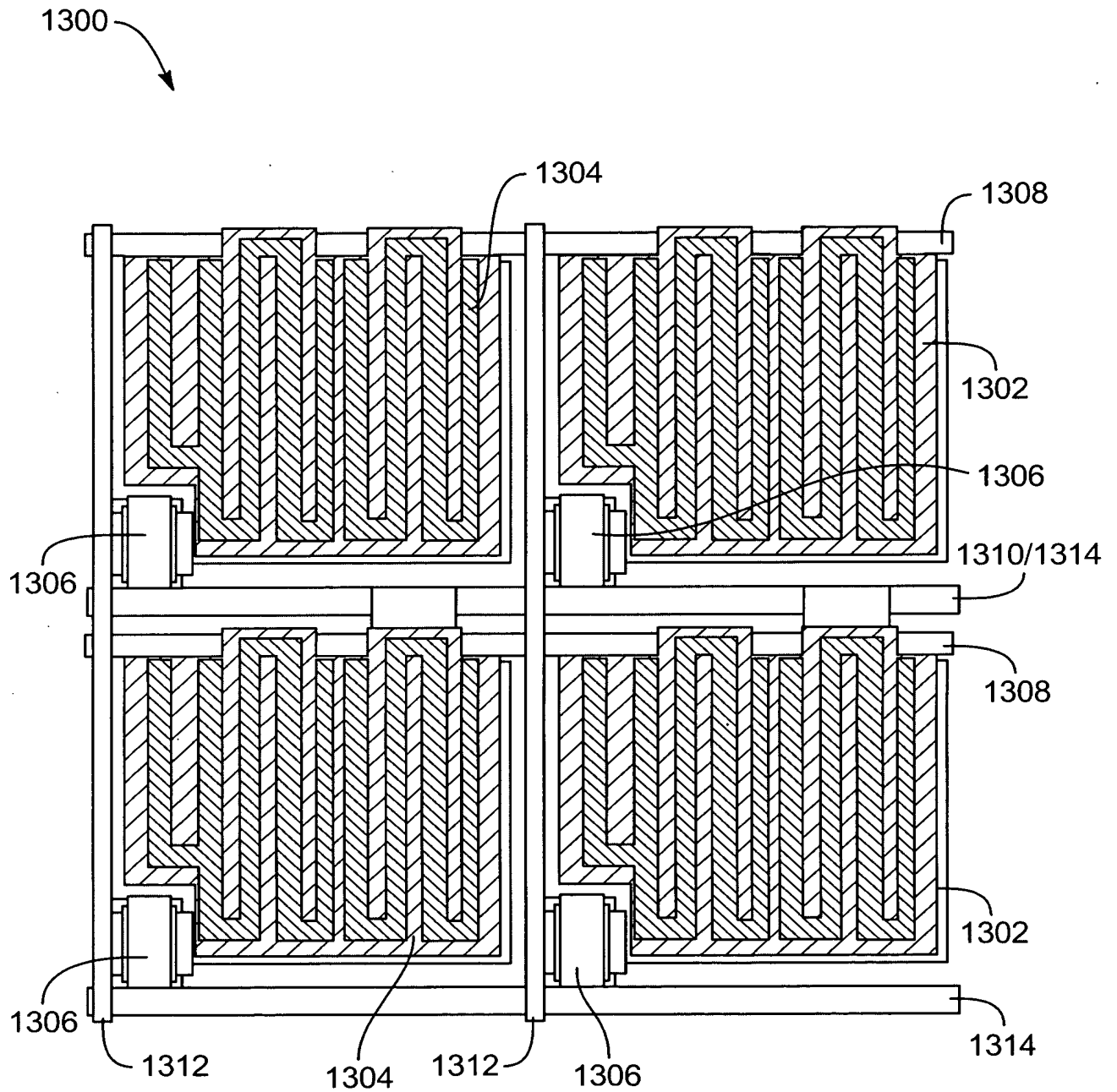


FIG. 24